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10/666,001 Examiner

R. Stephen Dildine

Applicant(s)/Patent under Reexamination

TSUCHINAGA, HIROYUKI

Art Unit

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Class	Subclass	Date	Examiner
714	769-771	4/19/2006	RSD
714	780-781	4/19/2006	RSD
714	784	4/20/2006	RSD
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	, see Search printout	8/9/2006	RSD

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US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB (text search, see Search History printout)	4/20/2006	RSD
IEEE Explore	4/20/2006	RSD
update all above	8/9/2006	RSD
Inventor's name search	4/20/2006	RSD